



St. Michael Polytechnic College

St. Santhiyagappar Nagar
Kalayarkoil-630 551.



DEPT: EEE

YEAR/SEMESTER: III / V

SUB.NAME: PROGRAMMABLE LOGIC CONTROLLER

Each question carries 1(one) mark in PART-A and 12(twelve) marks in PART-B

PART A

1. Define PLC.
2. Explain advantages of PLC.
3. Explain criteria for selection of suitable PLC.
4. Explain important part of PLC.
5. Explain pulse train.
6. List various PLC's available in market.
7. Explain PLC SCAN.
8. Explain application of PLC.
9. List the various types of PLC.
10. Mention the function of CPU in PLC.

PART B

1. Discuss in detail about advantages of PLC over hardwired system.
2. Draw the basic block diagram of PLC and give brief account on each block.
3. Discuss in details about the PLC operation.
4. Explain the memory organization of standard PLC's.
5. Explain PLC evaluation (6 mark)(page no1.02), Draw and explain power supply of PLC(6 mark).